# PocketBeagle PRU Input P1/P2 Header PinMux Modes

| PRU 0 Inputs | P1/P2 Pin | Offset | mode0 | mode1 | mode2 | mode3 | mode4 | mode5 | mode6 | mode7 |
|---|---|---|---|---|---|---|---|---|---|---|
| PRU0_R31_16* | P1.20 | 0x9B4 | xdma_event_intr1 | - | tclkin | clkout2 | timer7 | pr1_pru0_pru_r31_16 | EMU3 | gpio0_20 |
| PRU0_R31_7 | P1.29 | 0x9AC | mcasp0_ahclkx | eQEP0_strobe | mcasp0_axr3 | mcasp1_axr1 | EMU4 | pr1_pru0_pru_r30_7 | pr1_pru0_pru_r31_7 | gpio3_21 |
| PRU0_R31_4 | P1.31 | 0x9A0 | mcasp0_aclkr | eQEP0A_in | mcasp0_axr2 | mcasp1_aclkx | mmc0_sdwp | pr1_pru0_pru_r30_4 | pr1_pru0_pru_r31_4 | gpio3_18 |
| PRU0_R31_1 | P1.33 | 0x994 | mcasp0_fsx | ehrpwm0B | - | spi1_d0 | mmc1_sdcd | pr1_pru0_pru_r30_1 | pr1_pru0_pru_r31_1 | gpio3_15 |
| PRU0_R31_0 | P1.36 | 0x990 | mcasp0_aclkx | ehrpwm0A | - | spi1_sclk | mmc0_sdcd | pr1_pru0_pru_r30_0 | pr1_pru0_pru_r31_0 | gpio3_14 |
| PRU0_R31_16* | P2.09 | 0x984 | uart1_txd | mmc2_sdwp | dcan1_rx | I2C1_SCL | - | pr1_uart0_txd | pr1_pru0_pru_r31_16 | gpio0_15 |
| PRU0_R31_15 | P2.18 | 0x83C | gpmc_ad15 | lcd_data16 | mmc1_dat7 | mmc2_dat3 | eQEP2_strobe | pr1_ecap0_ecap_capin_apwm_o | pr1_pru0_pru_r31_15 | gpio1_15P |
| PRU0_R31_14 | P2.22 | 0x838 | gpmc_ad14 | lcd_data17 | mmc1_dat6 | mmc2_dat2 | eQEP2_index | pr1_mii0_txd0 | pr1_pru0_pru_r31_14 | gpio1_14 |
| PRU0_R31_6 | P2.28 | 0x9A8 | mcasp0_axr1 | eQEP0_index | - | mcasp1_axr0 | EMU3 | pr1_pru0_pru_r30_6 | pr1_pru0_pru_r31_6 | gpio3_20 |
| PRU0_R31_3 | P2.30 | 0x99C | mcasp0_ahclkr | ehrpwm0_synci | mcasp0_axr2 | spi1_cs0 | eCAP2_in_PWM2_out | pr1_pru0_pru_r30_3 | pr1_pru0_pru_r31_3 | gpio3_17 |
| PRU0_R31_2 | P2.32 | 0x998 | mcasp0_axr0 | ehrpwm0_tripzone_input | - | spi1_d1 | mmc2_sdcd | pr1_pru0_pru_r30_2 | pr1_pru0_pru_r31_2 | gpio3_16 |
| PRU0_R31_5 | P2.34 | 0x9A4 | mcasp0_fsr | eQEP0B_in | mcasp0_axr3 | mcasp1_fsx | EMU2 | pr1_pru0_pru_r30_5 | pr1_pru0_pru_r31_5 | gpio3_19 |
| | | | | | | | | | | |

| PRU 1 Inputs | P1/P2 Pin | Offset | mode0 | mode1 | mode2 | mode3 | mode4 | mode5 | mode6 | mode7 |
|---|---|---|---|---|---|---|---|---|---|---|
| PRU1_R31_9 | P1.02 | 0x8E4 | lcd_hsync | gpmc_a9 | gpmc_a2 | pr1_edio_data_in3 | pr1_edio_data_out3 | pr1_pru1_pru_r30_9 | pr1_pru1_pru_r31_9 | gpio2_23 |
| PRU1_R31_11 | P1.04 | 0x8EC | lcd_ac_bias_en | gpmc_a11 | pr1_mii1_crs | pr1_edio_data_in5 | pr1_edio_data_out5 | pr1_pru1_pru_r30_11 | pr1_pru1_pru_r31_11 | gpio2_25 |
| PRU1_R31_15 | P1.30 | ? | uart0_txd | spi1_cs1 | dcan0_rx | I2C2_SCL | eCAP1_in_PWM1_out | pr1_pru1_pru_r30_15 | pr1_pru1_pru_r31_15 | gpio1_11 |
| PRU1_R31_14 | P1.32 | ? | uart0_rxd | spi1_cs0 | dcan0_tx | I2C2_SDA | eCAP2_in_PWM2_out | pr1_pru1_pru_r30_14 | pr1_pru1_pru_r31_14 | gpio1_10 |
| PRU1_R31_10 | P1.35 | 0x8E8 | lcd_pclk | gpmc_a10 | pru_mii0_crs | pr1_edio_data_in4 | pr1_edio_data_out4 | pr1_pru1_pru_r30_10 | pr1_pru1_pru_r31_10 | gpio2_24 |
| PRU1_R31_16* | P2.11 | 0x980 | uart1_rxd | mmc1_sdwp | dcan1_tx | I2C1_SDA | - | pr1_uart0_rxd | pr1_pru1_pru_r31_16 | gpio0_14 |
| PRU1_R31_16* | P2.31 | 0x8A4 | xdma_event_intr0 | - | timer4 | clkout1 | spi1_cs1 | pr1_pru1_pru_r31_16 | EMU2 | gpio0_19 |
| PRU1_R31_8 | P2.35 | 0x8E0 | lcd_vsync | gpmc_a8 | gpmc_a1 | pr1_edio_data_in2 | pr1_edio_data_out2 | pr1_pru1_pru_r30_8 | pr1_pru1_pru_r31_8 | gpio2_22 |

The information above shows the Pins on the PocketBeagle P1 and P2 Headers which can be used in PRU0 or PRU1 as Inputs.

* Yes this appears to be correct. Either P1_20 or P2_09 can be used as an input for PRU0 R31.t16
* Yes this appears to be correct. Either P2_11 or P2_31 can be used as an input for PRU1 R31.t16

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Adapted from the open source Pocket Beagle System Reference Manual document.

OfItselfSo.com/BeagleNotes/UsingDeviceTreesToConfigurePRUIOPins.php

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